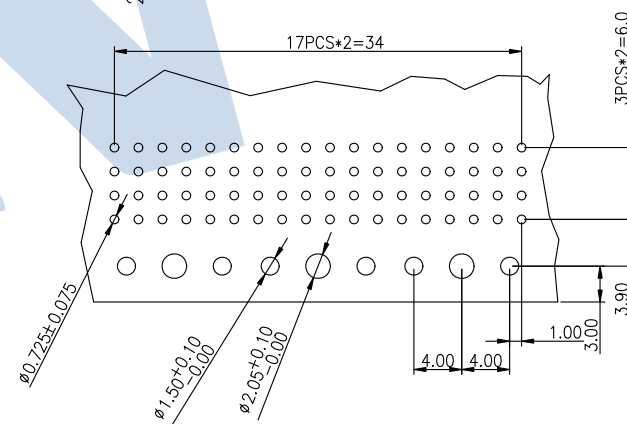
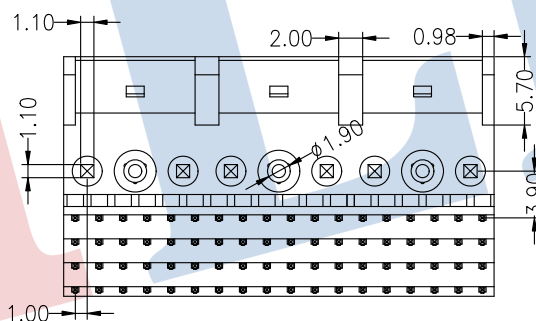


Packing
A:Tray
T:Tube



Recommended P.C.B Layout(Top Side)
(PCB BOARD TOLERANCE ± 0.05)

				OPERATION		DRAW	YiYuSheng	19.06.09	SCALE	FIT	JILN[®] 深圳市锦凌电子有限公司 SHENZHEN JINLING ELECTRONICS CO.,LTD Tel: 86-755-27483253 Fax: 86-755-2997-5588	
				X.X	±0.30					UNIT		
				X.XX	±0.20	CHECK			SIZE	A4	PART NO.	3510472AFRXXBWXX
A0	2019.06.09	NEW DRAWING		X.XXX	±0.10				SHEET	1/1		
REV	DATE	MODIFICATION DESCRIPTION	CHANGE	Angle	±3°	APPROVE			PROJ.		TITLE:	FUTURE BUS,4*18 Female 90°
				DIM	TOL							