



F

F

D

C

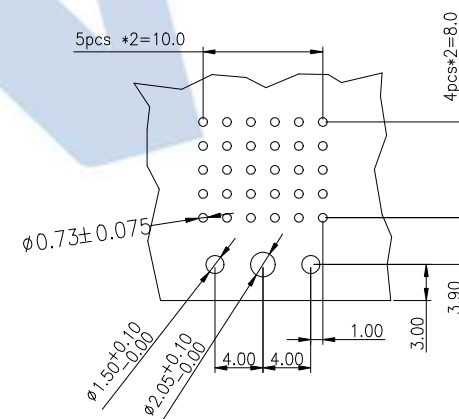
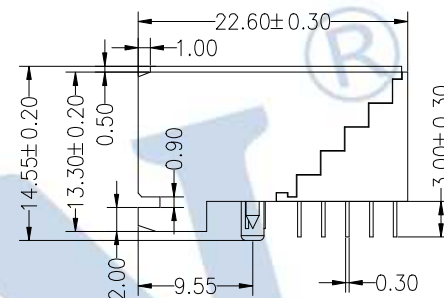
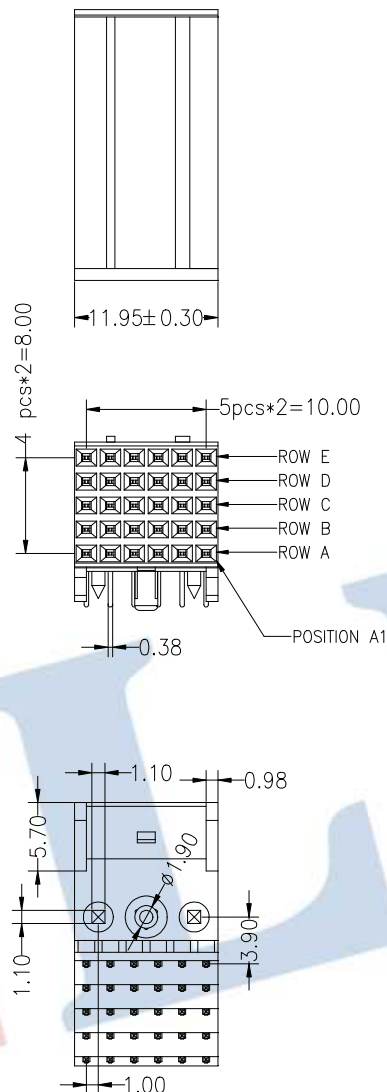
B

A

260° C $\pm$ 5° C, 10s(LCP insulator)

3510 5 30 A F R XX BW X X

Packing
A:Tray
T:Tube



Recommended P.C.B Layout(Top Side)
(PCB BOARD TOLERANCE ± 0.05)

				OPERATION		DRAW	YiYuSheng	19.10.09	SCALE	FIT	JiLN® 深圳市锦凌电子有限公司 SHENZHEN JINLING ELECTRONICS CO.,LTD Tel: 86-755-27483253 Fax: 86-755-2997-5588	
				X.X	±0.30				CHECK			
				X.XX	±0.20	APPROVE					SIZE	A4
A0	2019.10.09	NEW DRAWING		X.XXX	±0.10							SHEET
REV	DATE	MODIFICATION DESCRIPTION	CHANGE	Angle	±3°	DIM	TOL	PROJ.				

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PART NO.	3510530AFRXXBWXX
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TITLE:	FUTURE BUS.5*6 Female 90°
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